

[illegible]

Цель			Цель			Цель		
Конт	№проб		Конт	№проб		Конт	№проб	
-ЕНК	C1	55	+5U2	B1	15	+5U2	A1	15
+ЕНК	C2	206	+5U2	B2	15	+5U2	A2	15
-I	C3	156	1.4	B3	180		A3	
2AP51	C4	157	TEST	B4	181		A4	
WCS	C5	152	2AP52	B5	182		A5	
-I.1	C6	153	BUEN	B6	188		A6	
-I.2	C7	180	UMES	B7	244		A7	
-I.3	C8	161		B8			A8	
-I.3'	C9	162	2	B9	183		A9	
1..	C10	153	1.2	B10	184		A10	
2..	C11	164	1.1	B11	185		A11	
2.	C12	165	3..	B12	180		A12	
SRUT	C13	166	3.	B13	187		A13	
SRUT	C14	167		B14			A14	
A1150	C15	168		B15			A15	
PA150	C16	169		B16			A16	
R30	C17	170		B17			A17	
-AC12	C18	171	TXD	B18	188		A18	
C19				B19	185		A19	
+5U5EL	C20	15		B20			A20	
+15U2	C21	12		B21			A21	
OUT3	C22	194		B22	195		A22	
OUT2	C23	173	OUT4	B23	196		A23	
OUT0	C24	174	OUT1	B24	191		A24	
C25				B25	185		A25	
C26				B26	206		A26	
FS1	C27	75	04ELW	B27	192		A27	
SALAS1	C28	176	PAR.DN2	B28	192		A28	
C29				B29		SET3	A29	190
SET2	C30	177	SALAS1	B30	193	04ELW	A30	208
SET1	C31	178	B02	B31	14	B02	A31	14
F50	C32	79	B02	B32	14	B02	A32	14

The diagram illustrates the internal structure of a 40-pin ROM chip, showing the arrangement of memory cells and the connection of various pins. The chip is labeled with 'L6' and 'C3' at the top, and 'X7.2' on the right. The internal structure is divided into two main sections, each with a header row: 'UENR KONT NPROB' and 'UENR KONT NPROB'. The first section contains a grid of memory cells, with rows labeled 'UENR' (U0 to U15) and columns labeled 'KONT' (K0 to K15). The second section contains a grid of memory cells, with rows labeled 'UENR' (U0 to U15) and columns labeled 'KONT' (K0 to K15). The chip is connected to a 40-pin package, with pins 1 through 40 labeled. The truth table at the bottom shows the output of the chip for various input combinations.

UENR	KONT	NPROB
+SU	B1	
+SU	B2	
TW	B3	88
UPH1	B4	112
+15V	B5	8
PRCT1	B6	101
B0Z	B7	14
	B8	
	B9	
NET1	B10	183
	B11	
+15V	B12	12
LDPS2	B13	62
LDPS1	B14	57
NKS	B15	58
	B16	
	B17	
PRCT1	B18	101
3	B19	87
2	B20	85
AI190	B21	68
AI190	B22	68
	B23	55
4	B24	56
OUT4	B25	95
OUT2	B26	73
HLSSH	B27	92
2	B28	61
SEL1	B29	76
SET1	B30	78
0U1	B31	8
0U1	B32	8

UENR	KONT	NPROB
+SU	A1	
	A2	
	A3	
	A4	
	A5	
PRCT1	A6	
	A7	
	A8	
	A9	
	A10	
	A11	
	A12	
	A13	
	A14	
	A15	
	A16	
	A17	
	A18	
TEST	A19	81
	A20	
	A21	
	A22	85
1	A23	96
0.UT5	A24	
2	A25	83
OUT3	A26	94
SEL1	A27	152
SET1	A28	163
OUT1	A29	81
SET2	A30	77
OUT0	A31	8
0U1	A32	8
0U1	A33	8

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XTS.2			
Kont	Knob	Item	Kont
B1	20C	-15U_EN	R1
B2	206	-15U_EN	R2
B3			R3
B4			R4
B5			R5
B6	128	CEN1	R6
B7			R7
B8	121	CEN2	R8
B9			R9
B10	145	CG	R10
B11			R11
B12			R12
B13			R13
B14			R14
B15			R15
B16			R16
B17			R17
B18			R18
B19			R19
B20	208	0U_EN	R20
B21			R21
B22			R22
B23			R23
B24			R24
B25			R25
B26			R26
B27			R27
B28	207	-15U	R28
B29	207	-15U	R29
B30			R30
B31			R31
B32			R32

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	ПОДРОБНОСТИ	
	C6...C9	Ko
	C1....CA	Ko
	C5	KH
	L1, L2, L6, L7	D
	L3, L8	D
		BK
	X71,	B
	X72	B
	X73, X74, X77	A
	X78, X79,	
	X710	
	X75, X76	P
0	502-757	757
2	489-3	757
3	174-3	757
Реш		Реш
МН	Вопросы	Вопросы
Рубраж	См. рубраж	См. рубраж

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